High-Count SCALER

APU2508

200MHz up SCALER

It can TDC or SCALER high count signals above 200MHz and has various measurement modes such as start-stop, trend, and gate. The minimum time step of the histogram can be set from 20ns to 25sec by the application.

Features

- Input signal: NIM, TTL, etc.
- Input frequency: 200MHz and more
- Time step: 20ns to 25sec
- Histogram: 48bit x 4k
- Operation mode: start-stop, trend, gate
- Communication I/F: TCP/IP, Gigabit ethernet

Block diagram



Max + 10V Variable FPGA 1Gbps 1kΩ TTL ₩ 32 hist Ethernet Ethernet High data LAN PSC Speed РМА 8input 0 50Ω 10Gbps ctr NIM Comp optical Jumper 2Fast AUX threshold Ethernet 2ns setting ±5V Max-5V GATE РНУ CLK (O

start-stop mode



gate mode (external trend)



TechnoAP Co., Ltd.

2976-15 Mawatari, Hitachinaka, Ibaraki, Japan Postcode:312-0012 <u>info@techno-ap.com</u> TEL:+81-29-350-8011 FAX: +81-29-352-9013

trend mode



Operation application









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