

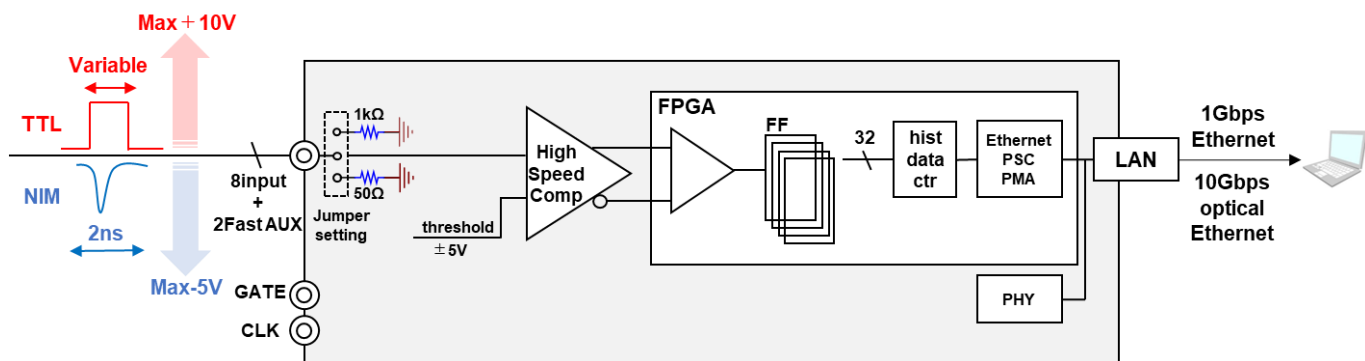
It can TDC or SCALER high count signals above 200MHz and has various measurement modes such as start-stop, trend, and gate. The minimum time step of the histogram can be set from 20ns to 25sec by the application.

Features

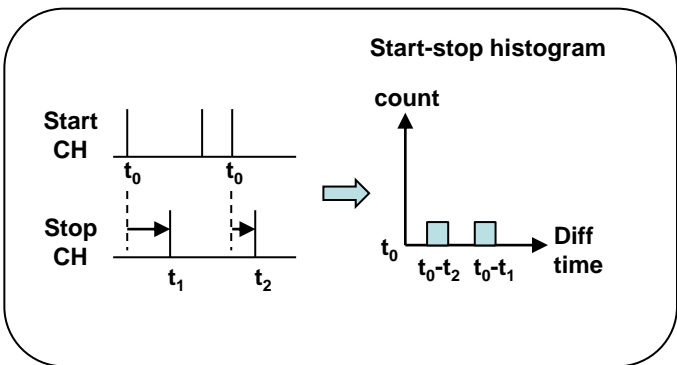
- Input signal: NIM, TTL, etc.
- Input frequency: **200MHz and more**
- Time step: 20ns to 25sec
- Histogram: 48bit x 4k
- Operation mode: start-stop, trend, gate
- Communication I/F: TCP/IP, Gigabit ethernet



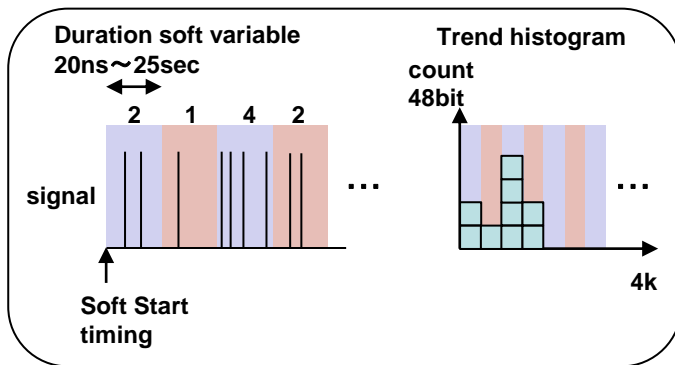
Block diagram



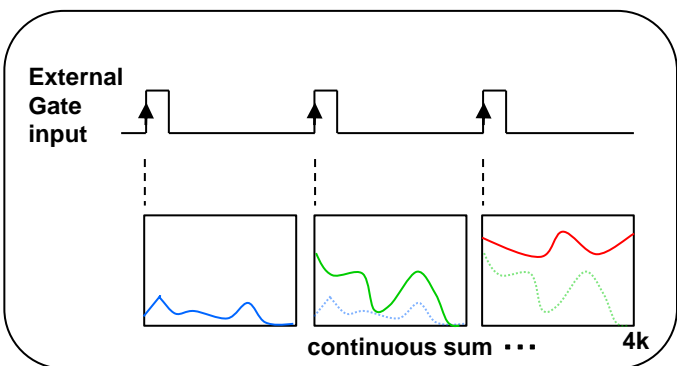
start-stop mode



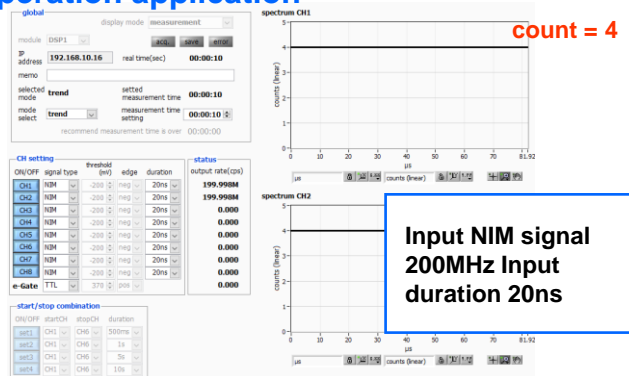
trend mode



gate mode (external trend)



Operation application



*Images is for illustration purpose.
*Please note that contents may change without prior notice.

