

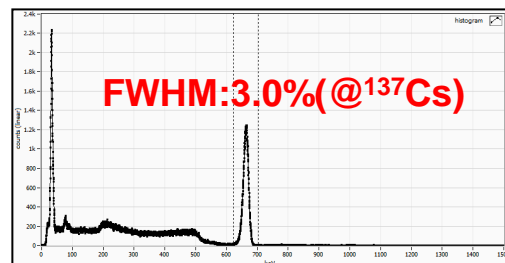
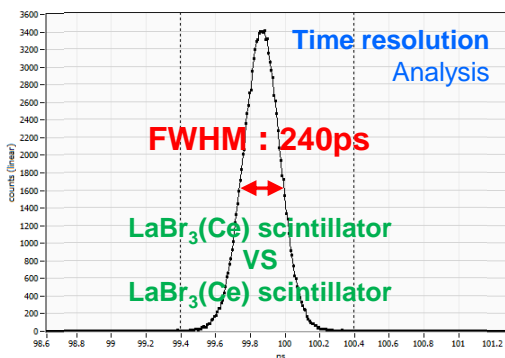
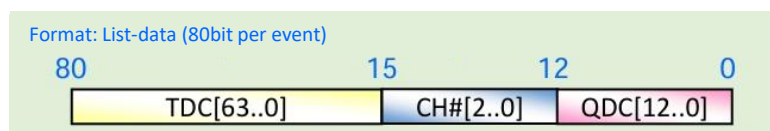
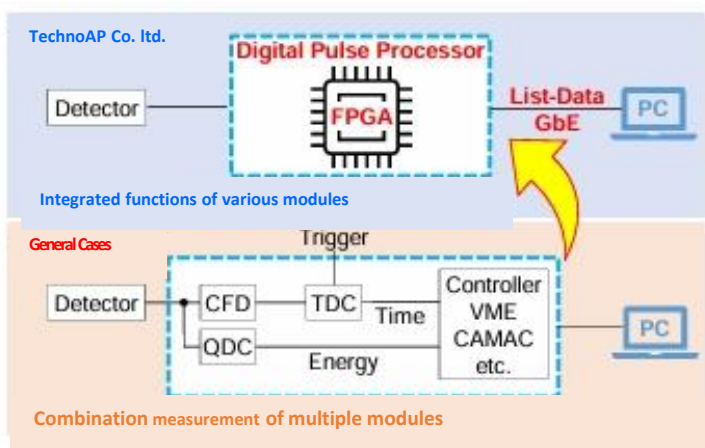
This waveform analysis board is equipped with a high-speed, high-resolution ADC (1 Gps, 14-bit). In addition to **real-time analysis** at 1 GHz using FPGA, the adoption of Gigabit Ethernet communication enables high-speed processing without dead time due to signal processing, achieving high temporal resolution and high throughput. All ADCs operate synchronously with a 1 GHz clock, making it suitable for signal analysis from multiple high-speed scintillation detectors.

Features

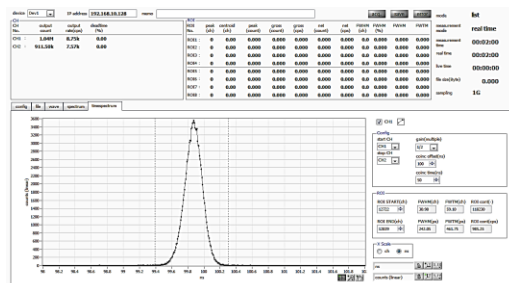
- ADC **1 Gps**, 2-channel input, 14-bit resolution
- Time Resolution **Coarse: 1 ns | Fine: 3.9 ps**
- Output 1 Mcps or more per channel
- Analysis Mode List (TDC + QDC, etc.)
- Function (Digital) CFD, TDC, QDC, PSD
- Options **LIST-WAVE, Coincidence**
- Communication I/F 1000BASE-T TCP/IP



Built-in power supply and stand-alone operation.



Energy resolution Analysis



Application window for data acquisition

Sample programs are provided

Specifications

Analog signal input type	Anode signal by PMT and Fast-NIM signal etc.
Analog signal input range	±1V, *Can be modified up to ±4V Input impedance: 50Ω. LEMO connector ×2
Analog offset adjustment	±2V, 14-bit, *Can be modified from ±20mV to ±4V
Analog gain switching	x1 and x3, *Up to x10 can be custom-made within the limits of the input range.
Analog signal rise time	Less than 1ns (@Gain x 1)
External input / output terminal (TTL level)	CLK input, CLK output, GATE input, VETO input, CLR input, OR output (LEMO connector ×6) *Input/output signal customizable
Communication Interface	1000BASE-T (TCP/IP)
External dimensions Weight	W:300 mm x H:56 mm x D:335 mm Approximately 3300 g

*Images is for illustration purpose.
*Please note that contents may change without prior notice.

