

Up to 32 channels of analog pulse can be converted at high speed using a switched capacitor ADC

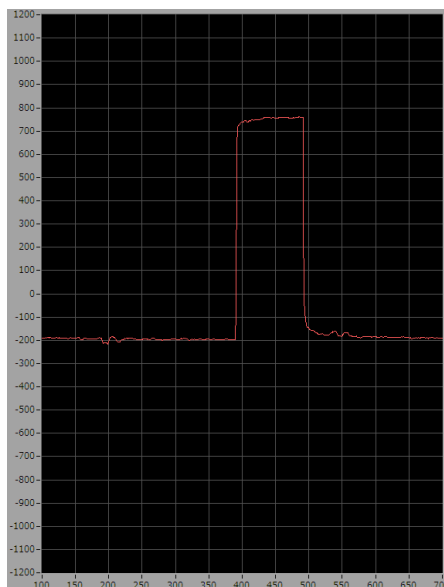
Operation Mode

1. WAVE mode

Read continuously the waveform data of up to 1024 points input from outside via Ethernet.

2. DUMP mode

Waveform data of up to 1024 points input from outside via Ethernet is temporarily stored in memory and then read.



Specifications

Analog Input	32 channel, single ended Connector: HIF3BA-34PA-2.54DS
Input Range	±10 V Variable gain per channel: ×0.5, ×1, ×2, ×5, ×10
Frequency Bandwidth	800 MHz
ADC	DRS-4 chip Paul Scherrer Institute design
Sampling Frequency	1G / 5Gsp/s
ADC Resolution	12-bit
ADC Memory Buffer	1024 cells
Memory	1 Gbyte (512 Mbyte x2)
Communication I/F	Ethernet RJ45 x2 TCP/IP, 1000 BaseT supported
Dimensions	1 width of VME 6U 20(W) x 262(H) x 187(D) mm
Weight	About 435 g

Thomson scattering measurement
Waveform digitizing

*Images is for illustration purpose.

*Please note that contents may change without prior notice.

