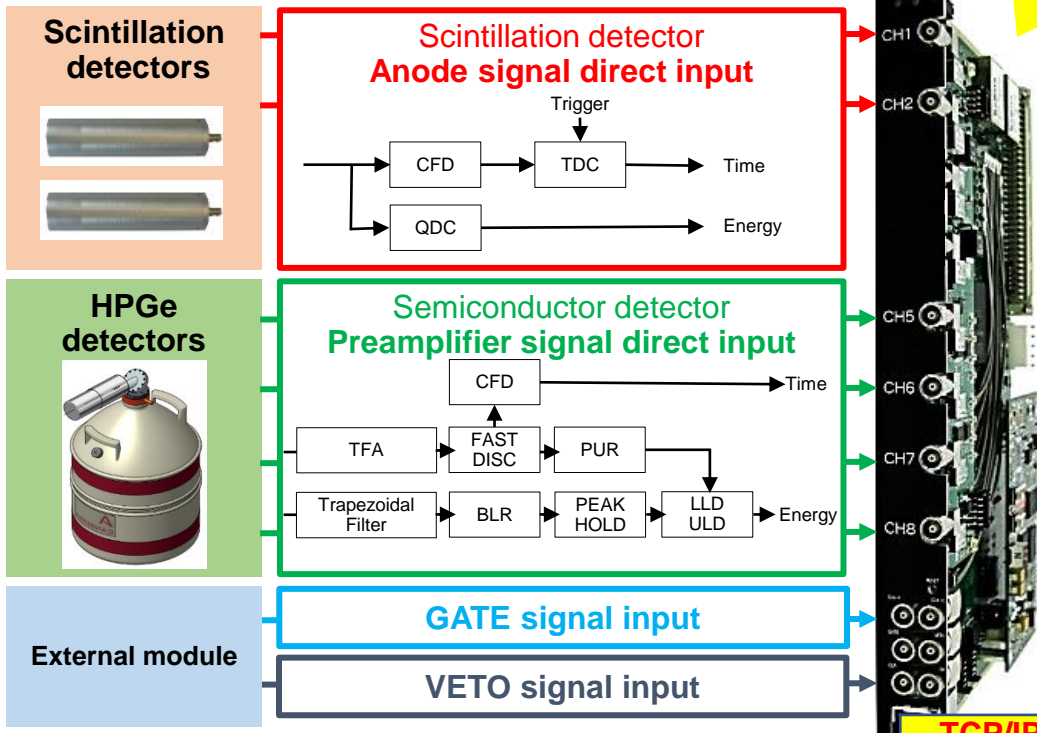


Features

Digital signal processing online at the same time for each signal from the scintillation detector and semiconductor detector with one unit.



Next-generation measurement module

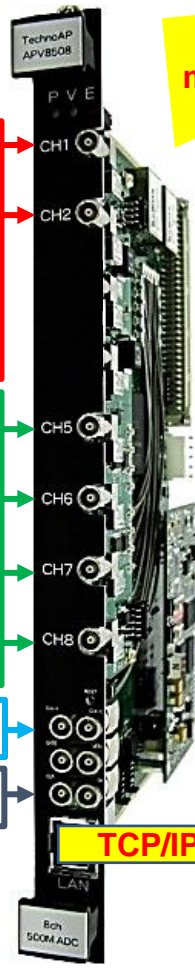
Channels 1 to 2 in the upper row can directly input the anode signal of PMT for coincidence or anti-coincidence.

Channels 5 to 8 in the lower row input the preamplifier signal of the semiconductor detector and are digitally processed at 62.5 MHz

Equipment control & data collection device

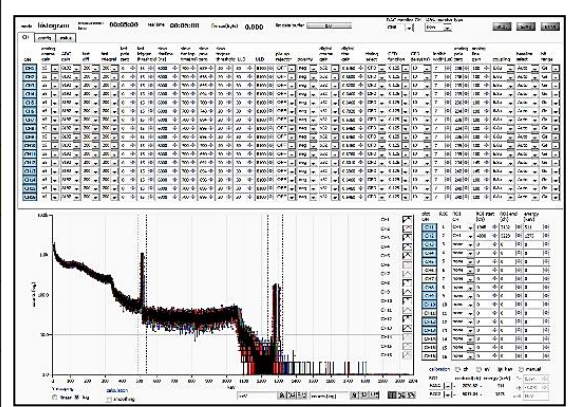


Recommended OS Windows 8.1 or later



Specifications

	1 to 2 CH *Upper row DPP part	3 to 6 CH *Lower row DSP part
Input	2 channel LEMO Input Impedance: 50 Ω Range: ±1 V	4 channel LEMO Input Impedance: 1 kΩ Range: ±2 V
Course Gain	x1, x3	x1, x2, x5, x10
ADC	1GHz Resolution 14-bit	62.5MHz Resolution 14-bit
ADC GAIN	4k, 2k, 1k, 512, 256 ch.	16k, 8k, 4k, 2k, 1k, 512, 256 ch.
Filter	QDC	Trapezoidal Filter 0.1 - 12 μs *0.01 step
Digital Processing	CFD, TDC, QDC *All parameters set by PC	Baseline Restorer, Pileup Rejecter, CFD *All parameters set by PC
External Terminal	Output filtered waveform, Input CLOCK, Input GATE, Input VETO, Input CLEAR *2 terminals for function expansion	
Communication I/F	TCP/IP, Gigabit Ethernet	
Dimension *digit: mm	VME6U: 20(W) x262(H) x187(D), Weight: 460 g Desktop: 300(W) x56(H) x335(D), Weight: 3360 g	



Application screen *histogram

Desktop type can be provided.



*Images is for illustration purpose.
*Please note that contents may change without prior notice.

