

XSDD50-04WL-SYS SYSTEM

The latest SDD adopts a 4-element configuration, achieving high counting rate and high energy resolution. The APU504XDC allows for the selection of **high counting rate mode** or **high resolution mode**, enabling flexible measurements.

Silicon Drift Detector
XSDD50-04WL

Window material: Windowless

Total sensitive area: 200 mm²
collimated to 50 mm² × 4 elements
Total element area: 260 mm²
65 mm² × 4 elements



Customizable

- Flange (ICF114)
- Tube length: 200 mm (default)

*Modifications will incur additional costs.

High Counting Signal Processing Module
APU504XDC



100 Msp/s, 16-bit



Screen of the included application

Power supply for detector
APU3900



-200 V, ±5V, +3.3V

Specification

Throughput	Maximum 150 kcps : 2 us Maximum 1000 kcps : 0.15 us
Measurement Mode	Histogram, List, Waveform, ROI-SCA
Energy resolution (typ.)	126 eV@5.9 keV MnKa, at 1 μs peaking time 1000 kcps OCR @ high counting setting
Communication	Gigabit Ethernet (TCP/IP)
Accessories	Data collection app, user manual Command manual, sample program
Option	Z-axis up/down mechanism, UHV valve, etc.



LEMO cable with both ends for signal(50cm)



Power and fan cables(3m)

*Images is for illustration purpose.
*Please note that contents may change without prior notice.

